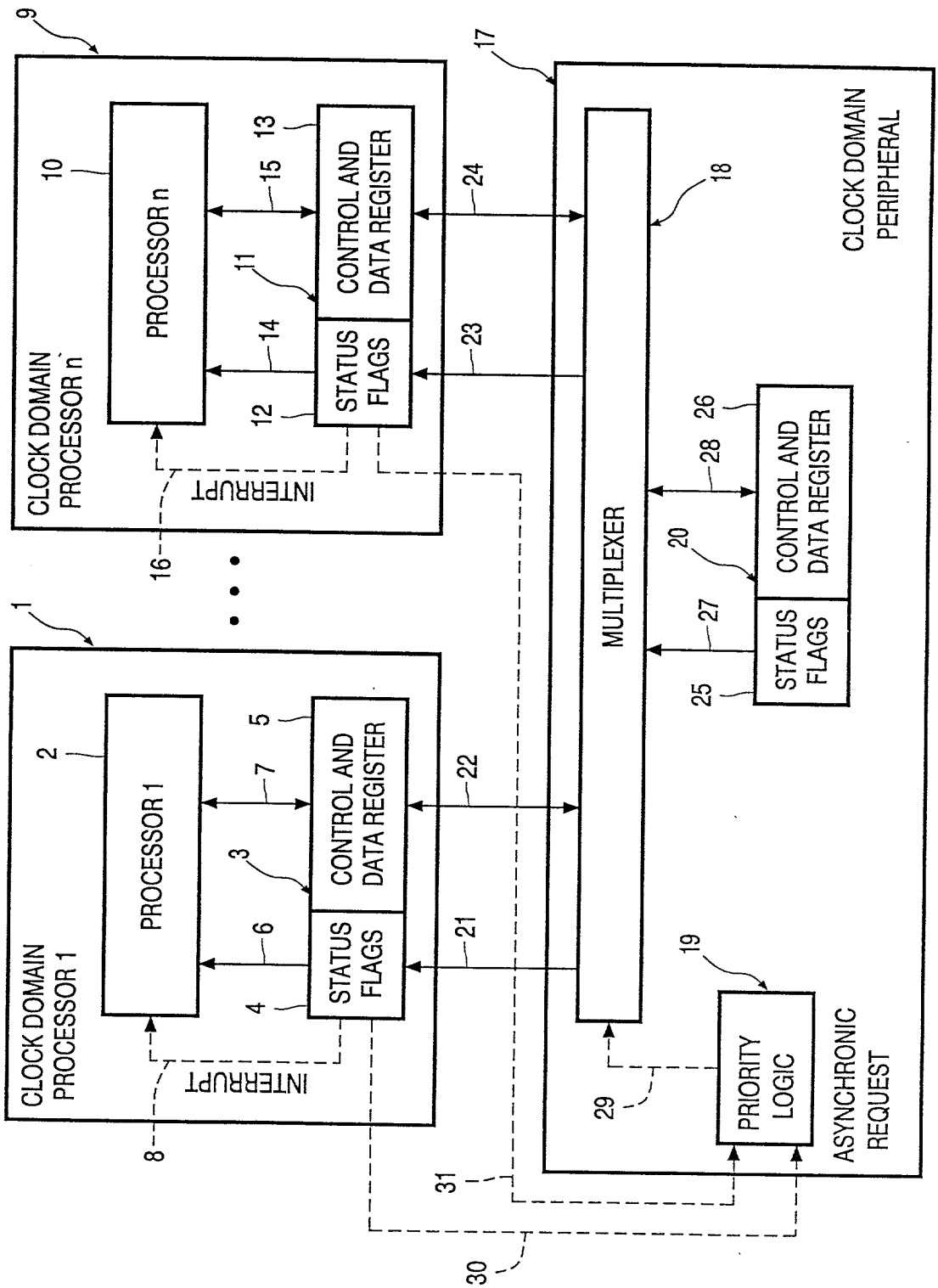
CLOCK DOMAIN PROCESSOR 1
PROCESSOR 1
CONTROL AND DATA REGISTER
STATUS FLAGS
INTERRUPT
CLOCK DOMAIN PROCESSOR n
PROCESSOR n
CONTROL AND DATA REGISTER
STATUS FLAGS
INTERRUPT
MULTIPLEXER
CLOCK DOMAIN PERIPHERAL
CONTROL AND DATA REGISTER
STATUS FLAGS
PRIORITY LOGIC
ASYNCHRONIC REQUEST
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